



Ref. No: SVEC/ECE/Circular/2023-24/45

CLASS CONSOLIDATED TIME TABLE

Class: III Semester

w.e.f.09-10-2023

Section: A

Class Coordinator: **Sri J. Rajendra**

Room No.: D-302

Periods	1	2	3	4	1:00-2:00	5	6	7
Time Day	9:30-10:30	10:30-11:20	11:20-12:10	12:10-1:00		2:00-2:50	2:50-3:40	3:40-4:30
Mon	SS	EDCA	PTSP	NT	Lunch Break	Data Structures Lab		
Tue	NT	EDCA	SS	CA		Data Structures Lab		
Wed	CA	SS	NT	EDCA		Data Structures Lab		
Thu	PTSP	PCS-I		EDCA		Data Structures Lab		
Fri	EDCA	NT	SS	PTSP		Data Structures Lab		
Sat	PCS-I	CA	NT	PTSP		Data Structures Lab		

Section: B

Class Coordinator: **Dr.T.V.N.L.Aswini**

Room No.: D-303

Periods	1	2	3	4	1:00-2:00	5	6	7
Time Day	9:30-10:30	10:30-11:20	11:20-12:10	12:10-1:00		2:00-2:50	2:50-3:40	3:40-4:30
Mon	CA	NT	SS	EDCA	Lunch Break	Data Structures Lab		
Tue	PTSP	NT	CA	EDCA		Data Structures Lab		
Wed	NT	EDCA	CA	SS		Data Structures Lab		
Thu	EDCA	NT	SS	PTSP		Data Structures Lab		
Fri	CA	PTSP	PCS-I			Data Structures Lab		
Sat	PTSP	NT	EDCA	SS		Data Structures Lab		

Section: C

Class Coordinator: **Sri R.Ram Prasad**

Room No.: D-304

Periods	1	2	3	4	1:00-2:00	5	6	7
Time Day	9:30-10:30	10:30-11:20	11:20-12:10	12:10-1.00		2:00-2:50	2:50-3:40	3:40-4:30
Mon	Data Structures Lab				Lunch Break	PTSP	NT	EDCA (T)
Tue	Data Structures Lab					CA	NT	PTSP
Wed	Data Structures Lab					EDCA	SS	NT
Thu	Data Structures Lab					EDCA	PTSP	NT (T)
Fri	Data Structures Lab					CA(T)	SS (T)	CA
Sat	Data Structures Lab					EDCA	PCS-I	SS

Staff Details:

S. No.	Sub	COURSENAME	SECTION		
			A	B	C
1.	EDCA	Electronic Device Circuit & Analysis	Sri.D.Venkanna Babu	Sri.D.Venkanna Babu	Sri R.Ram Prasad
2.	SS	Signals & Systems	Dr.T.V.N.L.Aswini	Dr.T.V.N.L.Aswini	Dr.Purnima K Sharma
3.	PTSP	Probability Theory & Stochastic Process	Sri J.Rajendra	Sri J.Rajendra	Sri D.R.Sandeep
4.	NT	Net Work Theory	Sri K.N.H.Srinivas	Sri V. Jaya Prakash	Dr.TDNSS.Sarveswara Rao
5.	CA	Complex Analysis (M-III)	Ms.S.Sirisha	Ms.P.Sravani	Smt.B.Adhilakshmi
6.	EDCA LAB	Electronic Device Circuit & Analysis Lab	Sri.D.Venkanna Babu Sri P.V.V.Rajesh Sri G.V.Subrahmanyam Sri Thota Sreenivas	Sri.D.Venkanna Babu Sri P.V.V.Rajesh Sri G.V.Subrahmanyam Sri Thota Sreenivas	Sri R.Ram Prasad Dr.B.Ashok Kumar Sri P.V.V.Rajesh Sri V. Jaya Prakash
7.	SS LAB	Signals & Systems Lab	Sri S.Kamesh Dr.T.V.N.L.Aswini Sri J.Rajendra	Dr.T.V.N.L.Aswini Sri J.Rajendra Sri V. Jaya Prakash	Dr.Purnima K Sharma Sri S.Kamesh Sri D.R.Sandeep
8.	DS LAB	Data Structures Lab	Sri V.Anil Kumar	Sri V.Anil Kumar	Dr.TDNSS.Sarveswara Rao
9.	PCS-I	Professional communication Skills-I	Mr. M.Venkata Ramana Mrs.U.Aparanjani	Mr. M.Venkata Ramana Mrs. Ch.Tanuja	Mrs.K.Radha Madhavi Ms.A.Kiranmayee

E. Kumaresh
Head of the Department

Vision

- To develop the department into a centre of excellence and produce high quality, technically competent and responsible Electronics and communication engineers

Mission

- To create a learner centric environment that promotes the intellectual growth of the students.
- To develop linkages with R & D organizations and educational institutions for excellence in teaching, learning and consultancy practices.
- To build the student community with high ethical standards.



CLASS CONSOLIDATED TIME TABLE

Class: III Semester

w.e.f.09-10-2023

Section: ECT

Class Coordinator: **Sri M. Pitchaiah**

Room No.:305

Periods	1	2	3	4	1:00-2:00	5	6	7
Time Day	9:30-10:30	10:30-11:20	11:20-12:10	12:10-1.00		2:00-2:50	2:50-3:40	3:40-4:30
Mon	Data Structures Lab				Lunch Break	EDCA	CA	NT
Tue	Data Structures Lab					EDCA (T)	SS	PTSP
Wed	Data Structures Lab					CA(T)	NT	CA
Thu	Data Structures Lab					PCS-I		PTSP(T)
Fri	Data Structures Lab					SS	EDCA	NT
Sat	Data Structures Lab					SS	PTSP	CA

Staff Details:

S. NO.	SUB	COURSE NAME	SECTION
			ECT
1.	EDCA	Electronic Device Circuit & Analysis	Sri R.Ram Prasad
2.	SS	Signals & Systems	Dr.Purnima K Sharma
3.	PTSP	Probability Theory & Stochastic Process	Sri M.Pitchaiah
4.	NT	Net Work Theory	Sri D.R.Sandeep
5.	CA	Complex Analysis(M-III)	Ms.S.Sirisha
6.	EDCA LAB	Electronic Device Circuit & Analysis Lab	Sri R.Ram Prasad Dr. B.Ashok Kumar Sri Thota Sreenivas Sri V. Jaya Prakash
7.	SS LAB	Signals & Systems Lab	Sri D.R.Sandeep Dr.Purnima K Sharma Sri V. Jaya Prakash
8.	DS LAB	Data Structures Lab	Dr.TDNSS.Sarveswara Rao
9.	PCS-I	Professional Communication Skills-I	Dr. K.Venkatarao Ms. A.Kiranmayee

E. Kumaran
Head of the Department

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SRI VASAVI ENGINEERING COLLEGE (AUTONOMOUS)

Pedatadepalli, TADEPALLIGUDEM-534 101, W.G. Dist.

Department Of Electronics and Communication Engineering



Ref. No: SVEC/ECE/Circular/2023-24/32

CLASS CONSOLIDATED TIME TABLE

Class: V Semester

Section: A

Class Coordinator: **Dr. M. Koteswara Rao**

w. e. f. 04-09-2023

Room No.: D-202

Periods	1	2	3	4	1:00-2:00	5	6	7	
Time Day	9:30-10:30	10:30-11:20	11:20-12:10	12:10-1.00		2:00-2:50	2:50-3:40	3:40-4:30	
Mon	MPMC	VLSI	PCS(V)		Lunch Break	MPMC Lab			
Tue	AWP	JAVA- LAB				AC	VLSI	LIB	
Wed	PCS(A)		AWP	MPMC		AC	VLSI	Sports	
Thu	VLSI	MPMC	JAVA	AC		MPMC	AWP	Sports	
Fri	JAVA	VLSI Lab				MPMC	AC	AWP	
Sat	AC	AWP	AC	VLSI		AWP	MPMC	VLSI	

Section: B

Class Coordinator: **Sri K.Pasipalana Rao**

Room No.: D-203

Periods	1	2	3	4	1:00-2:00	5	6	7
Time Day	9:30-10:30	10:30-11:20	11:20-12:10	12:10-1.00		2:00-2:50	2:50-3:40	3:40-4:30
Mon	AWP	JAVA Lab			Lunch Break	AC	VLSI	Sports
Tue	VLSI	AC	VLSI	AWP		MPMC	LIB	AC
Wed	AWP	VLSI	PCS(A)			JAVA	MPMC	Sports
Thu	AC	AWP	VLSI	MPMC		VLSI Lab		
Fri	AC	MPMC	PCS(V)			VLSI	AWP	MPMC
Sat	MPMC	MPMC Lab				AC	JAVA	AWP

Section: C

Class Coordinator: **Sri M.Vinod Kumar**

Room No.: A-304

Periods	1	2	3	4	1:00-2:00	5	6	7	
Time Day	9:30-10:30	10:30-11:20	11:20-12:10	12:10-1:00		2:00-2:50	2:50-3:40	3:40-4:30	
Mon	AWP	AC	MPMC	VLSI	Lunch Break	MPMC	AWP	LIB	
Tue	VLSI	MPMC	PCS(V)			AWP	AC	Sports	
Wed	AC	AWP	VLSI	MPMC		VLSI Lab			
Thu	AC	JAVA- LAB				VLSI	PCS(A)		
Fri	JAVA	MPMC Lab				AC	VLSI	MPMC	
Sat	MPMC	VLSI	AWP	JAVA		AWP	AC	Sports	

Staff Details:

S. NO.	SUB	COURSE NAME	SECTION		
			A	B	C
1.	VLSI	VLSI	Smt. Y.SUJATHA	Smt. Y.SUJATHA	Sri P.GOPALA REDDY
2.	MPMC	Microprocessors & Microcontrollers	Dr. M.KOTESWARA RAO	Dr. M.KOTESWARA RAO	Sri P.V.V.SATYANARAYANA
3.	AC	Analog Circuits	Sri K. PASIPALANA RAO	Sri K. PASIPALANA RAO	Sri B. ASHOK KUMAR
4.	AWP	Antennas & Wave Propagation	Sri P.NAGARAJU	Sri G. SHANKARA BHASKARA RAO	Sri G.V.SUBRAHMANYAM
5.	VLSI lab	VLSI lab	Smt. Y.SUJATHA Dr. S.V.V. SATYANARAYANA Sri P.NAGARAJU	Sri P.V.V. SATYANARAYANA Smt. Y.SUJATHA Sri D.VENKANNA BABU	Sri P.GOPALA REDDY Sri P.V.V. SATYANARAYANA Sri B.MURALI KRISHNA
6.	MPMC Lab	Microprocessors & Microcontrollers Lab	Sri T.SREENIVASU Dr. M.KOTESWARA RAO Sri M.VINOD KUMAR	Dr. M.KOTESWARA RAO Sri G. SHANKARA BHASKARA RAO Sri M.VINOD KUMAR	Sri T.SREENIVASU Sri G.V.SUBRAHMANYAM Sri M.VINOD KUMAR
7.	OTJ Lab	OOPs Through Java	Sri P. UMA SANKAR Dr.TDNSS.SARVESWARA RAO	Sri T ANIL KUMAR REDDY Sri B.MURALI KRISHNA	Sri T ANIL KUMAR REDDY Sri B.MURALI KRISHNA
8.	PCS-III	Professional Communication Skills-III	Sri J.N.V.SOMAYAJULU (A) Sri A.KIRANMAYEE(V)	Sri T.H.S.SRINIVAS (A) DR.SUJANI TATA(V)	Sri J.N.V.SOMAYAJULU (A) Sri M.VENKATA RAMANA(V)

E. K. K. K.

Head of the Department

Vision: To develop the department into a centre of excellence and produce high quality, technically competent and responsible Electronics and Communication Engineers.

Mission:

- To create a learner centric environment that promotes the intellectual growth of the students.
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SRI VASAVI ENGINEERING COLLEGE (AUTONOMOUS)

Pedatadepalli, TADEPALLIGUDEM-534 101, W.G. Dist.

Department Of Electronics and Communication Technology



CLASS CONSOLIDATED TIME TABLE

Class: V Semester

w. e. f. 04-09-2023

Section: ECT

Class Coordinator: Dr. B. Ashok Kumar

Room No.: A-204

Periods	1	2	3	4		5	6	7
Time	9:30-10:30	10:30-11:20	11:20-12:10	12:10-1:00	1:00-2:00	2:00-2:50	2:50-3:40	3:40-4:30
Day								
Mon	MPMC	VLSI	PCS(V)		Lunch Break	AC	VLSI	AWP
Tue	AWP	JAVA	VLSI	MPMC		MPMC Lab		
Wed	JAVA	MPMC	AWP	AC		LIB	PCS(A)	
Thu	MPMC	VLSI Lab				AWP	AC	Sports
Fri	VLSI	AC	AWP	VLSI		JAVA Lab		
Sat	AWP	AC	MPMC	AC		VLSI	MPMC	Sports

Staff Details:

S. NO.	SUB	COURSE NAME	SECTION
			ECT
1.	VLSI	VLSI	Sri P.GOPALA REDDY
2.	MPMC	Microprocessors & Microcontrollers	Sri P.V.V.SATYANARAYANA
3.	AC	Analog Circuits	Sri B. ASHOK KUMAR
4.	AWP	Antennas & Wave Propagation	Sri G. SHANKARA BHASKARA RAO
5.	VLSI lab	VLSI lab	Sri P.GOPALA REDDY Sri. M.PITCHAIAH Dr. M. THAMARAI
6.	MPMC Lab	Microprocessors & Microcontrollers Lab	Sri R.L.R LOKESH BABU Sri P.V.V.SATYANARAYANA Sri G. SHANKARA BHASKARA RAO
7.	OTJ Lab	OOPs Through Java	Sri T ANIL KUMAR REDDY Sri V.ANIL KUMAR
8.	PCS-III	Professional Communication Skills-III	Sri T.H.S.SRINIVAS (A) Sri M.VENKATA RAMANA (V)

E. K. Srinivas

Head of the Department

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Department Of Electronics and Communication Engineering



Ref. No: SVEC/ECE/Circular/2023-24/33

CLASS CONSOLIDATED TIME TABLE

Class: VII Semester

w.e.f: 04-09-2023

Section: A

Class Coordinator: Dr. T.D.N.S.S. Sarveswara Rao

Room No: A-301

Periods	1	2	3	4	1:00-2:00	5	6	7
Time Day	9:30-10:30	10:30-11:20	11:20-12:10	12:10-1:00		2:00-2:50	2:50-3:40	3:40-4:30
Mon	CDICD	OCN	CMC	ML	Lunch Break	CDICD	LIB	MSC
Tue	OCN	CDICD	CMC	CMC		DIP	ML	MSC
Wed	ML	ML	CDICD	OCN		DIP	MSC	OCN
Thu	DIP	DIP	OCN	MSC		CMC	CMC	Sports
Fri	OCN	DIP	ML	MSC		CDICD	CDICD	ML
Sat								

Section: B

Class Coordinator: Sri Thota Sreenivas

Room No.: A-303

Periods	1	2	3	4	1:00-2:00	5	6	7
Time Day	9:30-10:30	10:30-11:20	11:20-12:10	12:10-1:00		2:00-2:50	2:50-3:40	3:40-4:30
Mon	SA	ML	CDICD	MSC	Lunch Break	DIP	ML	CMC
Tue	CMC	MSC	ML	CDICD		SA	CMC	DIP
Wed	CDICD	DIP	MSC	CMC		CDICD	SA	ML
Thu	SA	MSC	CDICD	CMC		ML	DIP	Sports
Fri	ML	CMC	SA	DIP		SA	MSC	LIB
Sat								

Section: C

Class Coordinator: Sri B. Murali Krishana

Room No.: A-304

Periods	1	2	3	4	1:00-2:00	5	6	7
Time Day	9:30-10:30	10:30-11:20	11:20-12:10	12:10-1:00		2:00-2:50	2:50-3:40	3:40-4:30
Mon	MSC	DIP	LIB	OCN	Lunch Break	CMC	CDICD	ML
Tue	CDICD	ML	DIP	CMC		MSC	DIP	ML
Wed	OCN	CMC	OCN	CDICD		DIP	MSC	Sports
Thu	CMC	DIP	ML	MSC		OCN	CMC	CDICD
Fri	MSC	ML	CDICD	CMC		DIP	OCN	MSC
Sat								

Staff Details:

S. No.	Course Code	Sub	Course Name	SECTION		
				A	B	C
1.	V20ECT20	DIP	Digital Image Processing	Sri. Thota Sreenivas	Sri. Thota Sreenivas	Sri. T. Sreenivasu
2.	V20ECT22	CMC	Cellular & Mobile Communication	Dr. E. Kusuma Kumari	Sri. PVV Rajesh	Sri. PVV Rajesh
3.	V20ECT25	CDICD	COMS Digital IC Design	Dr. SVV Satyanarayana	Dr. SVV Satyanarayana	Sri. B Murali Krishna
4.	V20ECTJOC14	ML	Machine Learning	Dr. M Thamarai	Dr. M Thamarai	Sri. S Kamesh
5.	V20ECTJOC02 (Elective)	OCN	Optical Communication Networks	Dr. TDNSS Sarveswararao	--	Sri. RLR Lokesh Babu
6.	V20ECTJOC04	MSC	Modern Satellite Communication	Sri. KNH Srinivas	Sri M. Vinod Kumar	Sri M. Vinod Kumar
7.	V20ECTJOC12 (Elective)	SA	Sensors & Applications	--	Sri. V Anil Kumar	--

E. Kusuma Kumari

Head of the Department

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SRI VASAVI ENGINEERING COLLEGE (AUTONOMOUS)

Pedatadepalli, TADEPALLIGUDEM - 534101, W.G. Dist.
Department Of Electronics and Communication Engineering



Ref. No: SVEC/ECE/Circular/2023-24/05

CLASS CONSOLIDATED TIME TABLE

Class: VII Semester

w.e.f: 15-06-2023

Section: ECT

Class Coordinator: Sri RLR Lokesh Babu

Room No: A-203

Periods	1	2	3	4	1:00-2:00	5	6	7
Time	9:30-10:30	10:30-11:20	11:20-12:10	12:10-1.00		2:00-2:50	2:50-3:40	3:40-4:30
Day					Lunch Break			
Mon	CDICD	CMC	SA	DIP		ML	OCN	SPORTS
Tue	DIP	SA	CDICD	OCN		ML	CMC	CDICD
Wed	CMC	CDICD	DIP	SA		OCN	ML	DIP
Thu	ML	CMC	CMC	OCN		SA	DIP	OCN
Fri	SA	LIB	OCN	ML		CMC	CDICD	ML
Sat								

Staff Details

S. No.	Course Code	Sub	Course Name	ECT
1.	V20ECT20	DIP	Digital Image Processing	Sri. T Sreenivasu
2.	V20ECT22	CMC	Cellular & Mobile Communication	Sri. P Nagaraju
3.	V20ECT25	CDICD	COMS Digital IC Design	Sri. B Murali Krishna
4.	V20ECTJOC14	ML	Machine Learning	Sri. S Kamesh
5.	V20ECTJOC02	OCN	Optical Communication Networks	Sri. RLR Lokesh Babu
6.	V20ECTJOC12	SA	Sensors & Applications	Sri. V Anil Kumar

E. Kumar
Head of the Department

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